



IFW

Attorney's Docket No.: 12816-093001 / S-2512

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al. Art Unit : 2811
Serial No. : 10/630,632 Examiner : Joseph H. Nguyen
Filed : July 29, 2003
Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING
SEMICONDUCTOR CIRCUIT MODULES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In response to the action mailed August 5, 2004, please consider the following
amendments and the accompanying remarks.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being
deposited with the United States Postal Service as first class mail with
sufficient postage on the date indicated below and is addressed to the
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

9/3/04
Date of Deposit

Signature

Irja Zarembok

Typed or Printed Name of Person Signing Certificate